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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	09989762
Filing Date	2001-11-19
First Named Inventor	Kenneth Y. Ogami
Art Unit	2123
Examiner Name	Osborne, Luke R.
Attorney Docket Number	CD01175M

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
	2	6825689	B1	2004-11-30	Snyder	Entire Document
	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
	4	7023257	B1	2006-04-04	Sullam	Entire Document
	5	6967511	B1	2005-11-22	Sullam	Entire Document
	6	6892322	B1	2005-05-10	Snyder	Entire Document
	7	6823282	B1	2004-11-23	Snyder	Entire Document
	8	7127630	B1	2006-10-24	Snyder	Entire Document

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9	6611220	B1	2003-08-26	Snyder	Entire Document
10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
11	6765407	B1	2004-07-20	Snyder	Entire Document
12	7386740	B1	2008-06-10	Kutz et al.	Entire Document
13	6952778	B1	2005-10-04	Snyder	Entire Document
14	7185162	B1	2007-02-27	Snyder	Entire Document
15	6854067	B1	2005-02-08	Kutz et al.	Entire Document
16	6614320	B1	2003-09-02	Sullam et al.	Entire Document
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19	7149316	B1	2006-12-12	Kutz et al.	Entire Document

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20	6981090	B1	2005-12-27	Kutz et al.	Entire Document
21	6603330	B1	2003-08-05	Snyder	Entire Document
22	6507214	B1	2003-01-14	Snyder	Entire Document
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31	6563391	B1	2003-05-13	Mar	Entire Document
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36	7117485	B1	2006-10-03	Wilkinson et al.	Entire Document
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U.S. PATENT APPLICATION PUBLICATIONS

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1	20050240917	A1	2005-10-27	Wu, Yuh-Cherng	Entire Document
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17	USPTO Notice of Allowance for 10/001,477 (CD01203) dated 11/10/08; 7 pages	<input type="checkbox"/>
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28	USPTO Final Rejection for 10/002,217 (CD01207) dated 02/06/08; 10 pages	<input type="checkbox"/>
29	"Micropower CMOS Temperature Sensor with Digital Output," Bakker et al., IEEE Journal of Solid-State Circuits, 1996; 3 pages	<input type="checkbox"/>
30	"WP 3.5: An Integrated Time Reference," Blauschild, Digest of Technical Papers, 1994; 4 pages	<input type="checkbox"/>
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34	ITO, SERGIO AKIRA AND CARRO, LUIGI; "A Comparison of Microcontrollers Targeted to FPGA-Based Embedded Applications", September 2000, Proceedings of 13th Symposium on Integrated Circuits and Systems Design, 18-24 September 2000; 6 pages	<input type="checkbox"/>
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37	ROBINSON, GORDON D: "Why 1149.1 (JTAG) Really Works", May 1994, Conference Proceedings Electro/94 International, 10-12 May 1994, Combined Volumes; 6 pages	<input type="checkbox"/>
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39	"PSoC technology complete changes 8-bit MCU system design", Cypress Microsystems, Inc. retrieved from <http://www.archive.org/web/20010219005250/http://cypressmicro.com/~t...>, Feb. 19, 2001; 21 pages	<input type="checkbox"/>
40	Specks et al., "A Mixed Digital-Analog 16B Microcontroller with 0.5MB Flash Memory, On-Chip Power Supply, Physical Network Interface, and 40V I/O for Automotive Single-Chip Mechatronics," IEEE, Feb. 9, 2000; 1 page	<input type="checkbox"/>
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43	Application Number 10/033,027 (CD00232M): "Microcontrollable Programmable System on a Chip;" Snyder; 117 pages	<input type="checkbox"/>

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